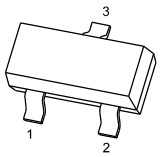
N-Channel 20-V(D-S) MOSFET

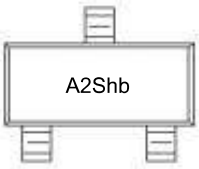
$V_{(BR)DSS}$	$R_{DS(on)}$MAX	I_D
20V	0.050Ω@4.5V	3A
	0.065Ω@2.5V	

SOT-23

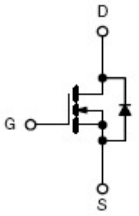


1.GATE
2.SOURCE
3.DRAIN

MARKING



Equivalent Circuit



General FEATURE

- TrenchFET Power MOSFET
- Lead free product is acquired
- Surface mount package

APPLICATION

- Load Switch for Portable Devices
- DC/DC Converter

Maximum ratings (T_a=25℃ unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	20	V
Gate-Source Voltage	V_{GS}	±10	
Continuous Drain Current	I_D	3.0	A
Pulsed Drain Current*1	I_{DM}	10	
Continuous Source-Drain Diode Current	I_S	1.0	
Maximum Power Dissipation	P_D	1.0	W
Thermal Resistance from Junction to Ambient(t ≤5s)	$R_{\theta JA}$	156	℃/W
Junction Temperature	T_J	150	℃
Storage Temperature	T_{stg}	-55 ~+150	

Note :

*1. Pulse Width ≤ 300μs, Duty cycle ≤2%

MOSFET ELECTRICAL CHARACTERISTICS

T_a =25 ℃ unless otherwise specified

Parameter	Symbol	Test Condition	Min	Typ	Max	Units
Static						
Drain-source breakdown voltage	$V_{(BR)DSS}$	V_{GS} = 0V, I_D =-250μA	20			V
Gate-source threshold voltage	$V_{GS(th)}$	V_{DS} =V_{GS}, I_D =-250μA	0.5	0.8	1.0	
Gate-source leakage	I_{GSS}	V_{DS} =0V, V_{GS} =±10V			±100	nA
Zero gate voltage drain current	I_{DSS}	V_{DS} =16V, V_{GS} =0V			1	μA
Drain-source on-state resistance [a]	$R_{DS(on)}$	V_{GS} =4.5V, I_D =3A		0.045	0.050	Ω
		V_{GS} =2.5V, I_D =2A		0.055	0.065	
Forward transconductance [a]	g_{fs}	V_{DS} =5V, I_D =3A		8.0	-	S
Dynamic[b]						
Input capacitance	C_{iss}	V_{DS} =10V,V_{GS} =0V,f =1MHz		300		pF
Output capacitance	C_{oss}			120		
Reverse transfer capacitance	C_{rss}			80		
Total gate charge	Q_g	V_{DS} =10V,V_{GS} =4.5V,I_D =3A		4.0		nC
Gate-source charge	Q_{gs}			0.65		
Gate-drain charge	Q_{gd}			1.6		
Turn-on delay time	$t_{d(on)}$	V_{DD}=10V,I_D =3A V_{GEN}=4.5V,Rg=6Ω		15.0		ns
Rise time	t_r			85.0		
Turn-off delay time	$t_{d(off)}$			45.0		
Fall time	t_f			20.0		
Drain-source body diode characteristics						
Continuous source-drain diode current	I_S	T_C=25℃			1.0	A
Body diode voltage	V_{SD}	I_S=1.0A		0.7	1.3	V

Notes :

a.Pulse Test : Pulse Width < 300μs, Duty Cycle ≤2%.

b.Guaranteed by design, not subject to production testing.

Typical Electrical and Thermal Characteristics

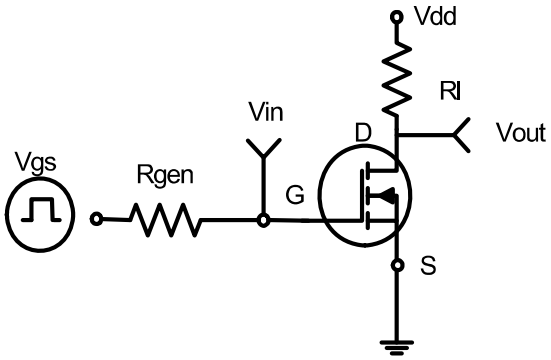


Figure 1:Switching Test Circuit

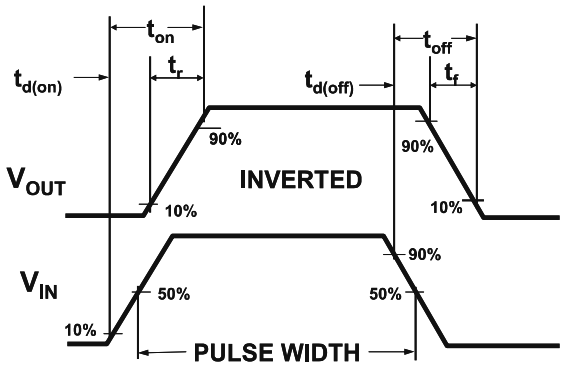


Figure 2:Switching Waveforms

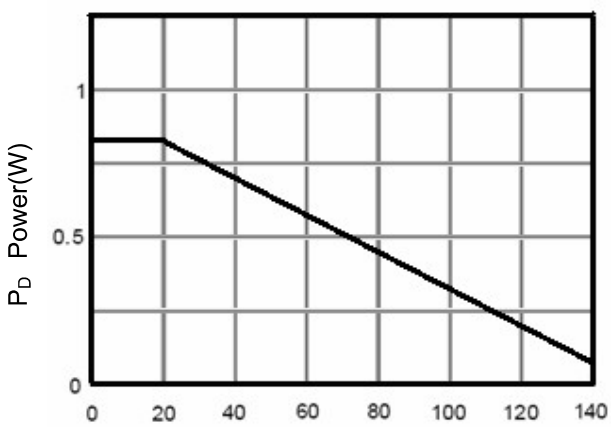


Figure 3 Power Dissipation

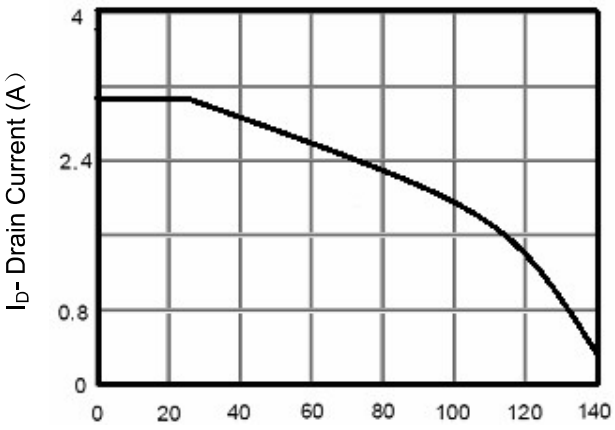


Figure 4 Drain Current

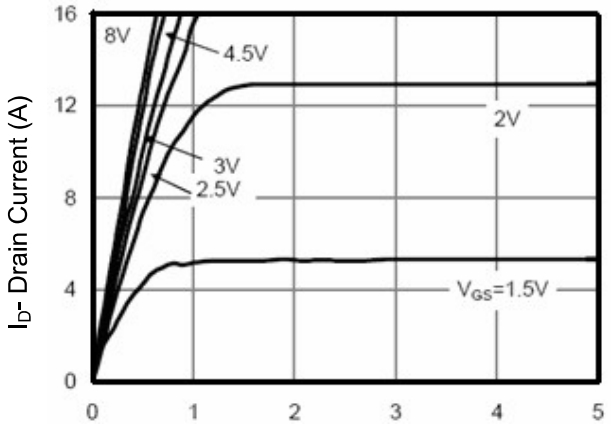


Figure 5 Output Characteristics

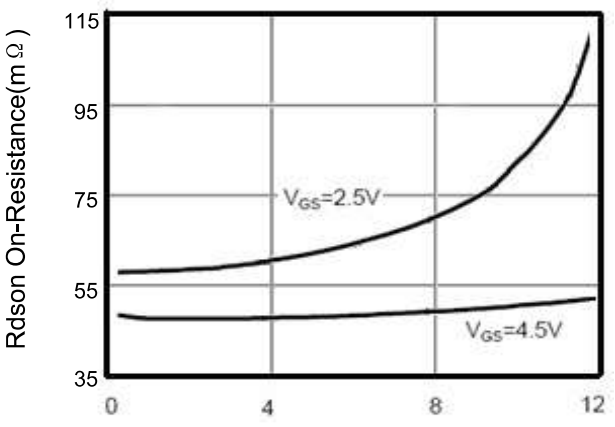
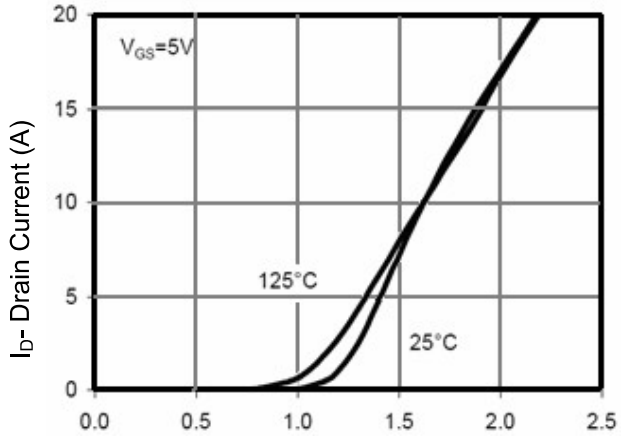
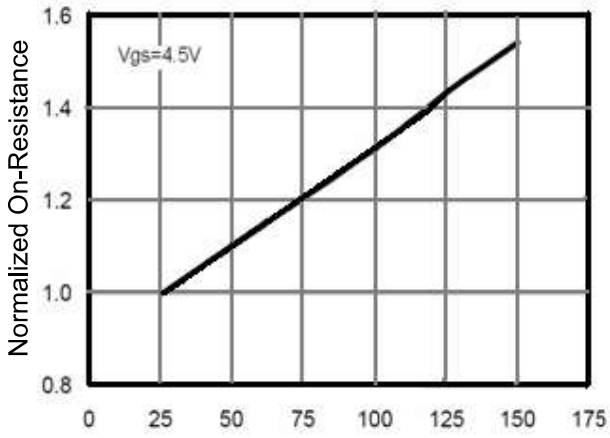


Figure 6 Drain-Source On-Resistance



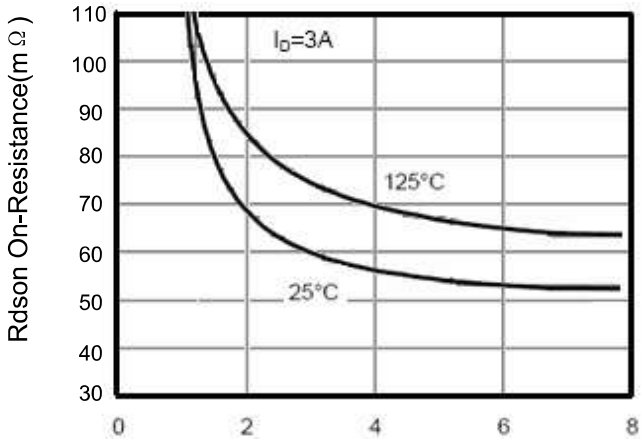
Vgs Gate-Source Voltage (V)

Figure 7 Transfer Characteristics



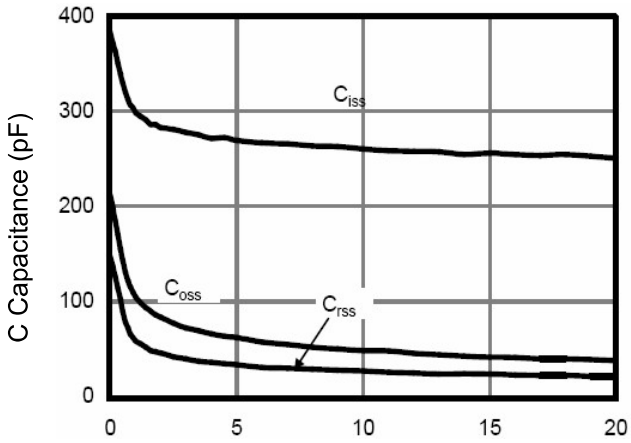
T_J-Junction Temperature(℃)

Figure 8 Drain-Source On-Resistance



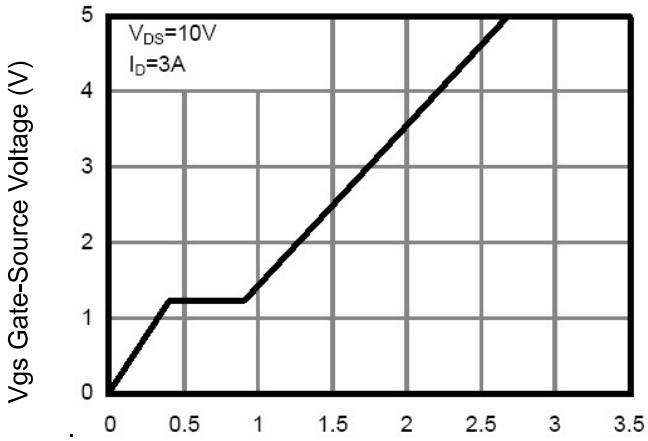
Vgs Gate-Source Voltage (V)

Figure 9 Rdson vs Vgs



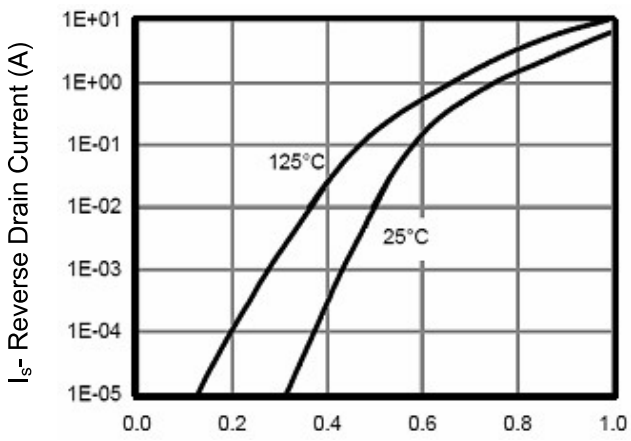
Vds Drain-Source Voltage (V)

Figure 10 Capacitance vs Vds



Qg Gate Charge (nC)

Figure 11 Gate Charge



Vsd Source-Drain Voltage (V)

Figure 12 Source- Drain Diode Forward

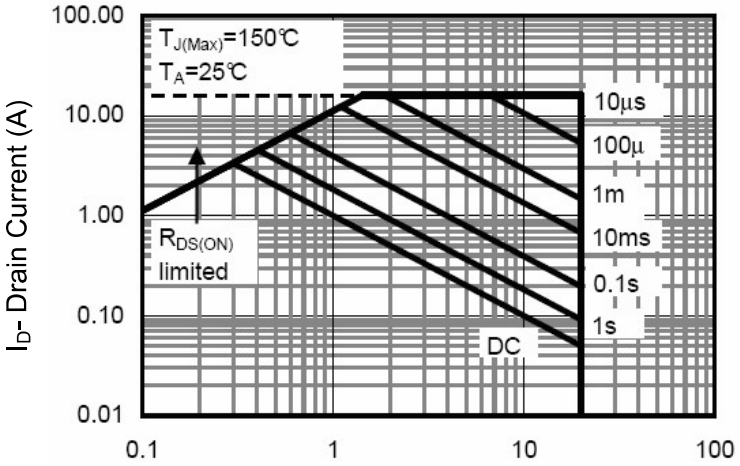


Figure 13 Safe Operation Area

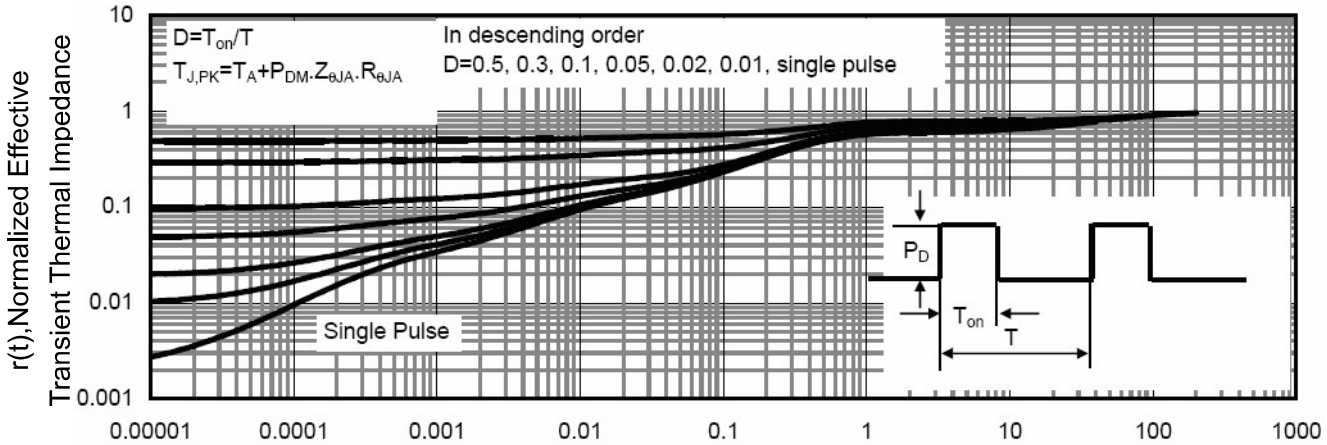
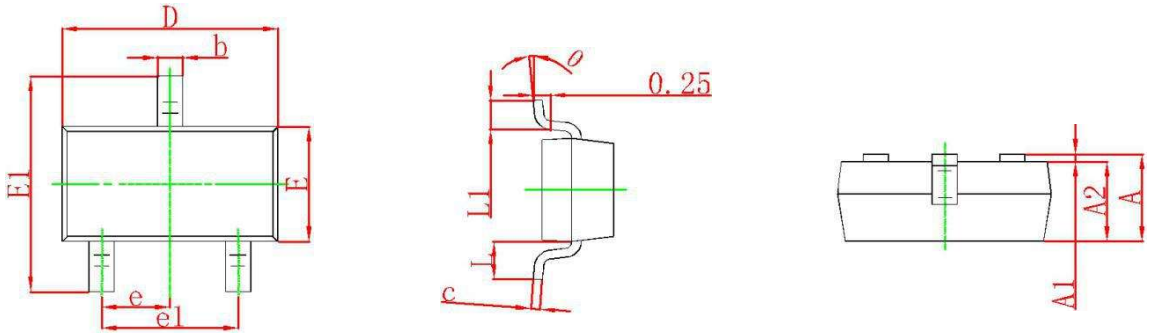


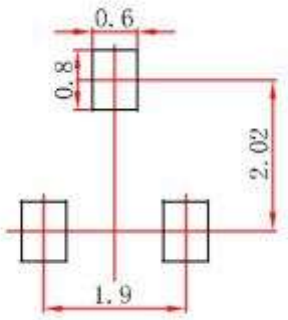
Figure 14 Normalized Maximum Transient Thermal Impedance

SOT-23 Package Outline Dimensions



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.900	1.150	0.035	0.045
A1	0.000	0.100	0.000	0.004
A2	0.900	1.050	0.035	0.041
b	0.300	0.500	0.012	0.020
c	0.080	0.150	0.003	0.006
D	2.800	3.000	0.110	0.118
E	1.200	1.400	0.047	0.055
E1	2.250	2.550	0.089	0.100
e	0.950 TYP		0.037 TYP	
e1	1.800	2.000	0.071	0.079
L	0.550 REF		0.022 REF	
L1	0.300	0.500	0.012	0.020
θ	0°	8°	0°	8°

SOT-23 Suggested Pad Layout



Note:
1.Controlling dimension:in millimeters.
2.General tolerance:±0.05mm.
3.The pad layout is for reference purposes only.